

Fig. 1 (prior art)



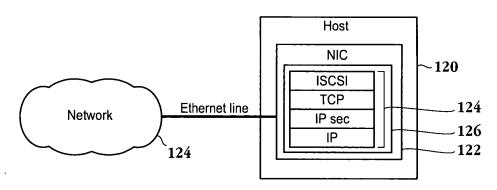


Fig. 2



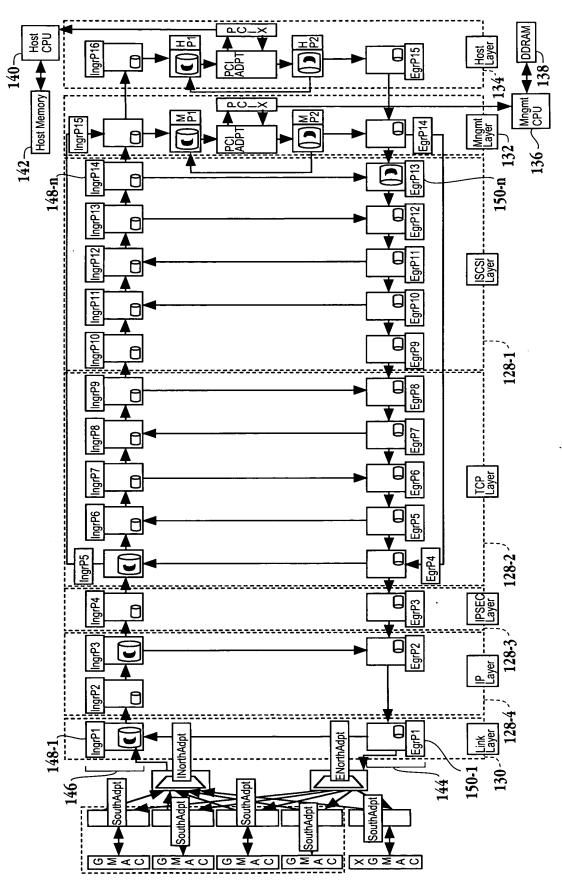
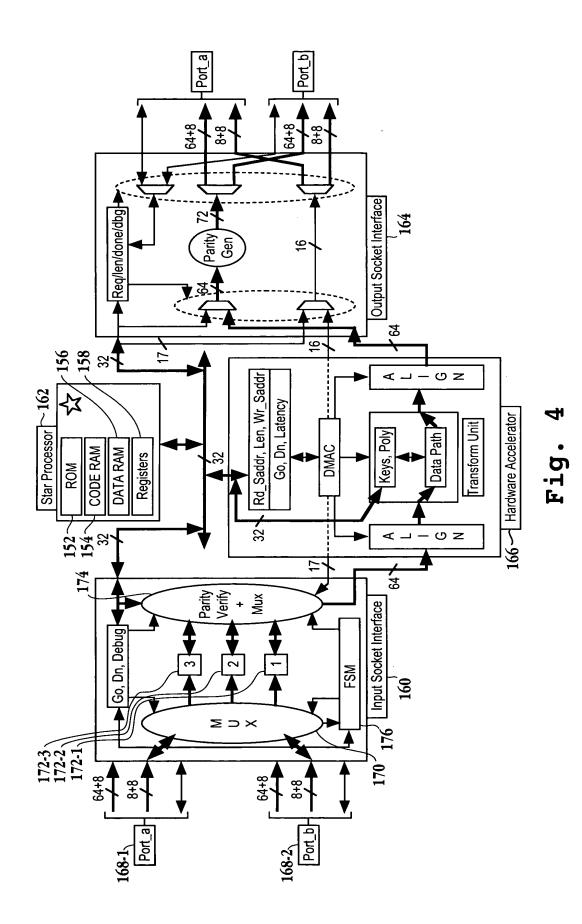


Fig. 3







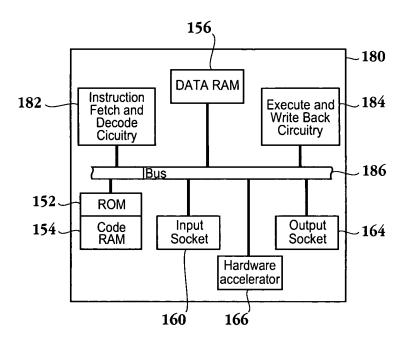
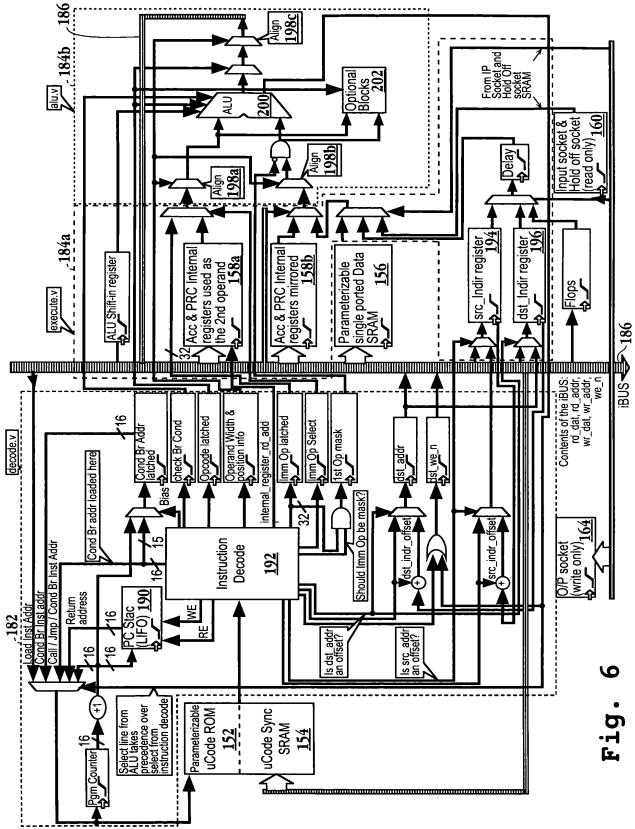


Fig. 5







I - instruction fetch and decode

II - execute and write back

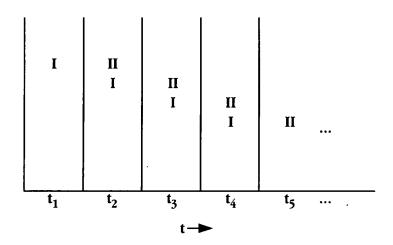


Fig. 7



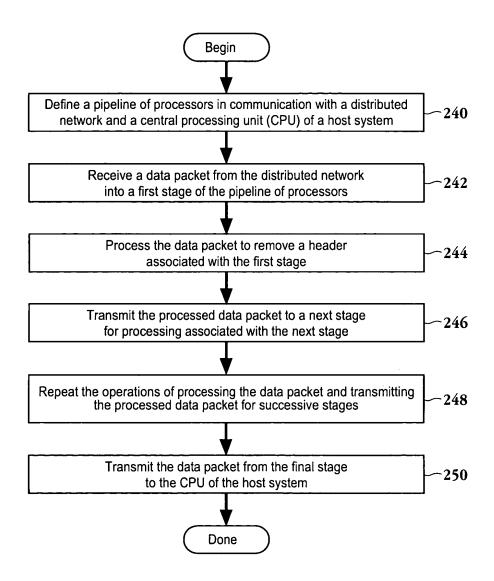


Fig. 8